



SUGGESTED PAD LAYOUT

ELECTRICAL CHARACTERISTICS:

- DCR : PIN 3.4-5.6 = 77mΩ MAX.
PIN 9.10-11.12 = 9.8mΩ MAX.
PIN 1-2 = 96mΩ MAX.
PIN 8-7 = 70mΩ MAX.
- INDUCTANCE : (@250kHz, 0.1Vrms)
PIN 3.4-5.6 = 75uH ±10%.
- LEAKAGE INDUCTANCE (@250kHz, 0.1Vrms)
PIN 3.4-5.6 (PIN 9.10-11.12 SHORT) = 0.76uH MAX.
- TURN RATIO (@100kHz, 0.1Vrms) :
PIN 3.4-5.6 : PIN 12.11-9.10 = 4 ±2%.
PIN 3.4-5.6 : PIN 2-1 = 4 ±2%.
PIN 3.4-5.6 : PIN 8-7 = 6.66 ±2%.
- HI-POT : (@1500VAC, 5mA, 2SEC) PIN2.3.4- PIN 8.11.12
- OPERATING TEMPERATURE : -20°C~+125°C
- RoHS COMPLIANT

							TITLE POE TRANSFORMER	
							DWG. NO. ATS-1239R	
RELEASE					UNITS:		SAFETY	
NO: DESCRIPTION					DATE		P/N:	
DATE					BY		DRAW	
CHK					APPD		SHEET 1 of 1	
REVISIONS							DRAW	